

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**  
Patent Application

Inventor(s): Adam Wilson and Ryan Jurasek

Group Art Unit: 2816

Filed: 03/24/04

Examiner: TRA

Application No.: 10808883

Title: METHOD AND CIRCUIT FOR ADJUSTING A REFERENCE VOLTAGE SIGNAL

**Form 1449**

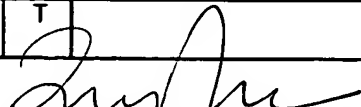
**U.S. Patent Documents**

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
<u>RA</u>	A	5,218,364	06/08/93	Kumazawa et al.	341	136	07/10/91
<u>RA</u>	B	5,216,385	06/01/93	McDaniel	330	282	08/10/92
<u>RA</u>	C	6,147,908	11/14/00	Abugharbieh et al.	365	185.2	11/03/97
<u>RA</u>	D	6,329,804	12/11/01	Mercer	323	315	10/13/99
<u>RA</u>	E	6,467,669	11/05/02	McClure et al.	327	543	07/10/01
<u>RA</u>	F	6,281,734	08/28/01	McClure et al.	327	308	12/31/99
	G						
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	J						
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**Foreign Patent or Published Foreign Patent Application**

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	L							
	M							
	N							
	O							
	P							
	Q							

**Other Documents**

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	R	
	S	
	T	
Examiner		
	Date Considered	<u>3/31/05</u>

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered.  
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